**Exam solution ideas**

**Problem 1**

1. Truth table examples of incomplete functions in : L2.5 , incomplete.
2. Only NAND, only NOR circuits are explained in L1.5
3. MoM is in L3.3 .
4. MoD and decoder expansion examples is in L3.3.
5. Plan B example schematics using capturing the full truth table or the circuit's flowchart are in LAB2.
6. Propagation delay and levels of gates in combinational networks: L4.3.
7. Power consumption is explained in L4.3
8. How to drive LED is found in L2.4 and basic electrical characteristics in L1.6.

**Problem 2**

1. A typical standard binary encoder using plan A is presented in L2.3 and in *Enc_10_4*
2. 
3. 

**Problem 3**

1. A typical 8-bit integer arithmetic circuit for 8bit is discussed in the highlighted project P4.
2. 
3. 
4. 

**Problem 4**

The programmable gate is another example of circuit that can be solved using plan C2 and the MoM.