SDLS100

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

These devices contain four independent 2-input OR gates.

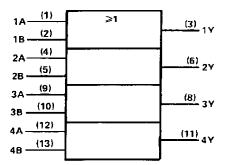
The SN5432, SN54LS32 and SN54S32 are characterized for operation over the full military range of -55°C to 125°C. The SN7432, SN74LS32 and SN74S32 are characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
A	B	Ŷ
н	х	н
х	н	н
L	L	L

logic symbol[†]

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[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

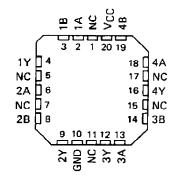
Pin numbers shown are for D. J. N. or W packages.

SN5432, SN54LS32, SN54S32, SN7432, SN74LS32, SN74S32 QUADRUPLE 2-INPUT POSITIVE-OR GATES DECEMBER 1983 - REVISED MARCH 1988

SN5432, SN54LS32, SN54S32 ... J OR W PACKAGE SN7432 . . . N PACKAGE SN74LS32, SN74S32 . . . D OR N PACKAGE (TOP VIEW)

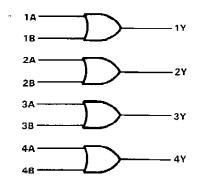
	1A [] 1B [] 1Y [] 2A [] 4 2B [] 5 2Y [] 6	14 VCC 13 4B 12 4A 11 4Y 10 3B
GND 7 8 3Y	2 \ 6	∍БзА

SN54LS32, SN54S32 ... FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram



positive logic

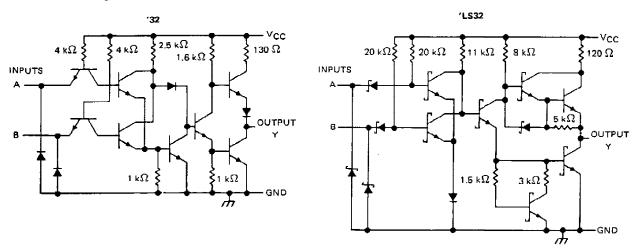
$$Y = A + B \text{ or } Y = \overline{\overline{A} \cdot \overline{B}}$$

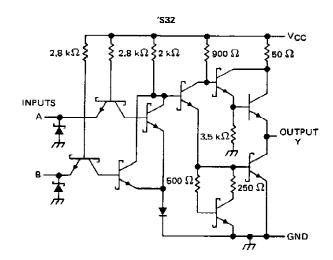
PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warrenty. Production processing does not necessarily include testing of all parameters.



SN5432, SN54LS32, SN54S32, SN7432, SN74LS32, SN74S32 QUADRUPLE 2-INPUT POSITIVE-OR GATES

schematics (each gate)





Resistor values shown are nominal.

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	
Input voltage: '32, 'S32	5.5 V
'L\$32	
Operating free-air temperature: SN54'	
SN74′	0°C to 70°C
Storage temperature range	
NOTE 1: Voltage values are with respect to network ground terminal.	



recommended operating conditions

			SN5432			SN7432			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
۷ін	Hgh-level input voltage	2			2			V	
VIL	Low-level imput voltage			0.8			0,8	v	
юн	High-level output current			- 0.8			~ 0.8	mA	
IOL	Low-level output current			16			16	mΑ	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

BADAMETER	TEST CONDITIONS †	SN5432	SN7432	UNIT
PARAMETER		MIN TYP‡ MAX	MIN TYP: MAX	UNIT
VIK	V _{CC} = MIN, I _I = - 12 mA	- 1.5	- 1,5	v v
V _{OH}	V _{CC} = MIN, V _{IH} ≈ 2 V, I _{OH} ≈ − 0.8 mA	2.4 3.4	2.4 3.4	V
VOL	V _{CC} = MIN, V _{1L} = 0.8 V, I _{OL} = 16 mA	0,2 0.4	0.2 0.4	V
- II	V _{CC} = MAX, V ₁ = 5.5 V	1	1	mΑ
!н	V _{CC} = MAX, V ₁ = 2.4 V	40	40	μA
IL.	V _{CC} = MAX, V ₁ = 0.4 V	1.6	- 1.6	mΑ
OSS	V _{CC} = MAX	- 20 - 55	- 18 - 55	mА
Іссн	V _{CC} = MAX, See Note 2	15 22	15 22	mA
	VCC = MAX, V1 = 0 V	23 38	23 38	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
‡ All typical values are at V_{CC} = 5 V, T_A = 25°C.
§ Not more than one output should be shorted at a time.

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NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, V_{CC} = 5 V, $T_A = 25^{\circ}C$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN	түр	МАХ	UNIT
^t PLH	A or 8	×	R 400 O	C. = 15 = 5		10	15	ris
^t PHL	7018	1	R _L = 400 Ω,	CL = 15 pF		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



SN54LS32, SN74LS32 QUADRUPLE 2 INPUT POSITIVE OR GATES

recommended operating conditions

			SN54LS32 SN74L		SN74LS	532		
		MIN	NOM	MAX	MIN	NOM		UNIT
V _{CC} Supply	r voltage	4.5	5	5.5	4.75	5	5.25	V
VIH Hgh-ley	vel input voltage	2			2			V
VIL Low-le	vel input voltage			0.7			0,8	V
OH High-le	evel output current			- 0,4			- 0.4	mĀ
OL Low-le	vel output current			4			8	mΑ
TA Operta	ting free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS †			SN54LS	32		SN74LS	32	
PARAMETER				MIN	TYP\$	MAX	MIN	TYP‡	MAX	
Vik	V _{CC} - MIN,	l ₁ = 18 mA				- 1.5			- 1.5	v
∨он	VCC = MIN,	V _{IH} = 2 V,	I _{OH} = - 0.4 mA	2.5	3.4	•	2.7	3.4		V
14	V _{CC} - MIN,	VIL = MAX,	IOL = 4 mA		0.25	0.4		0.25	0.4	v
VOL	VCC = MIN,	V _{IL} = MAX,	loL = 8 mA					0.35	0.5	, v
Π ₁	V _{CC} - MAX,	V ₁ = 7 V				0.1			0.1	mA
	VCC = MAX,	V _I = 2.7 V			•	20			20	μA
IIL.	V _{CC} = MAX,	VI = 0.4 V				- 0.4			- 0.4	mA
IOS§	VCC = MAX			- 20		- 100	- 20		- 100	mA
Іссн	V _{CC} = MAX,	See Note 2			3.1	6.2		3.1	6.2	mA
ICCL	VCC = MAX,	V ₁ = 0 V		l	4.9	9.8	I	4.9	9.8	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

f All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. § Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second. NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST COM	MIN	түр	МАХ	UNIT	
^t PLH	1 az 0	V	D DIG	0 - 15 -		14	22	ПS
^t PHL	A or B	1	$R_{L} = 2 k \Omega,$	CL = 15 pF		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



recommended operating conditions

			SN54S32			SN74S32			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
⊻ін	High-level input voltage	2			2			v	
VIL	Low-level input voltage			0.8			0.8	v	
юн	High-level output current			1			- 1	mΑ	
^I OL	Low-level output current			20			20	mΑ	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDIT		SN54S32				SN74S3	2	
PARAMETER		TEST CONDIT		MIN	TYP \$	MAX	MIN	TYP ‡	MAX	
VIK	VCC = MIN,	lj = – 18 mA				- 1.2			- 1.2	V
VOH	V _{CC} = MIN,	V _{IH} = 2 V,	IOH = - 1 mA	2.5	3.4		2.7	3.4		V
Vol	VCC = MIN,	V _{IL} = 0.8 V,	IOL = 20 mA			0.5			0.5	V
4	V _{CC} = MAX,	V ₁ = 5.5 V				1			1	mA
Чн	VCC = MAX,	V = 2.7 V				50		•	50	μA
ΊL	VCC = MAX,	Vi = 0.5 V				- 2			- 2	mA
los §	V _{CC} = MAX			- 40		— 1 00	- 40		- 100	mA
Іссн	V _{CC} = MAX,	See Note 2			18	32		18	32	mA
ICCL	VCC = MAX,	V1 = 0 V			38	68	<u> </u>	- 38	68	mA

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† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second. NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, VCC = 5 V, TA = 25° C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON		MIN	түр	MAX	UNIT
ΦLΗ		V	D 07 0 0	0 15 -5		4	7	ns
tPHL	A or B		R _L ≖ 280 Ω,	CL = 15 pF		4	7	ns
tPLH	A P	v	R _{I.} = 280 Ω,	C1 = 50 pF		5		пs
tPHL .	A or 8	Ŷ	nL - 200 32,	0 00 pr		5		ris.

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



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