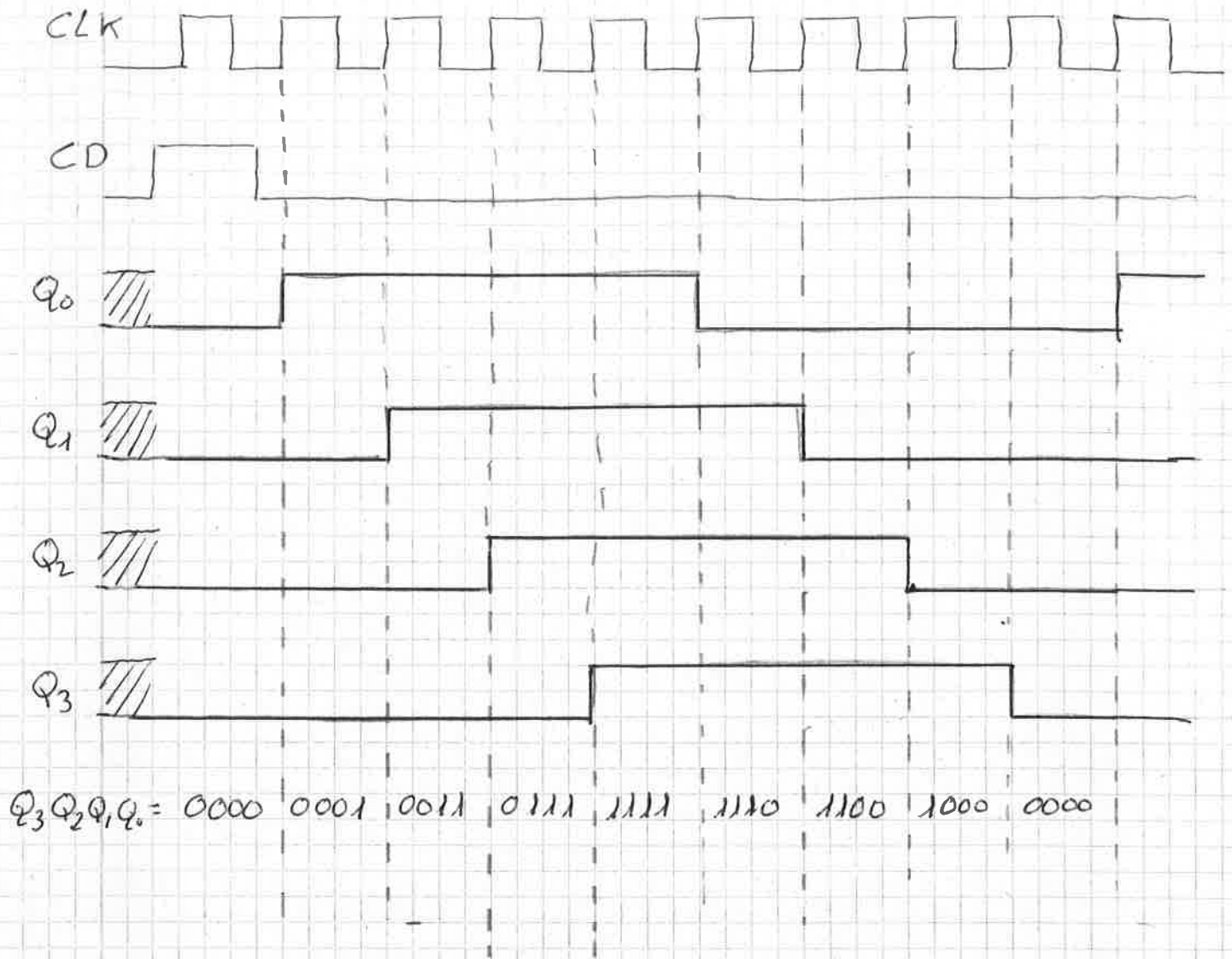


Problem 2



This circuit can be used to implement a Johnson counter of 4 bits

Two VHDL files will be required