

LAB 4.2 CONTROL SHEET

Date: _____

Group : G _____

Student names: _____

Pre-lab activities (Check if done)

	#1. Truth table, timing diagram, input vectors, operations, etc.
	#2. Quartus Prime project.
	#3. Functional simulation.
	#4. Timing analyser.
	#5. install the WaveForms software

Lab activities (Check if done)

	A. FPGA pin assignment
	B. USB-blaster driver
	C. Programmer tool
	D. Run the circuit and test operations
	E. Static power consumption
	F. Propagation delay
	G. Dynamic power consumption

Post lab assignment [PLA4_2](#), due date: November 7



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Additional notes: